

UTRAN UMTS Terrestrial Radio Access Network  
 CN Core Network  
 UE User Equipment

FIG 1

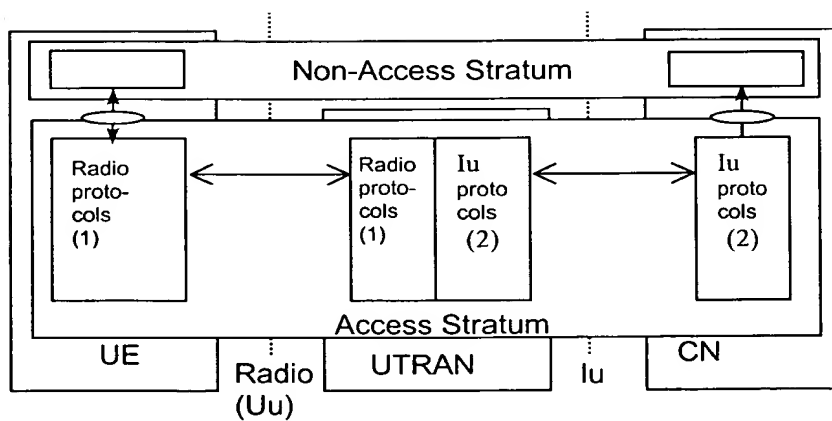


FIG 2

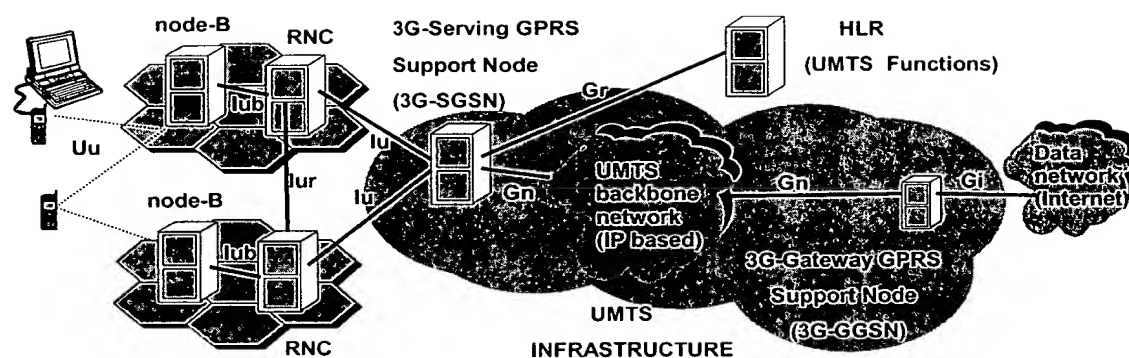
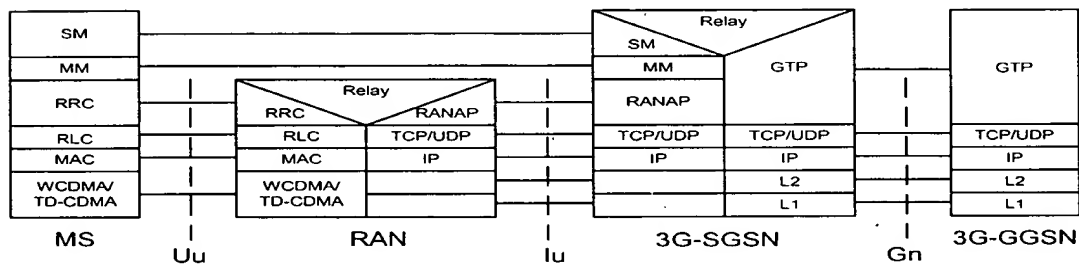
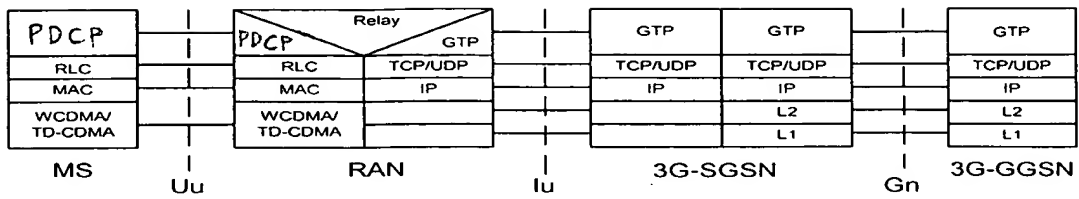


Fig. 3



008280" 18784960

008280" T8484960

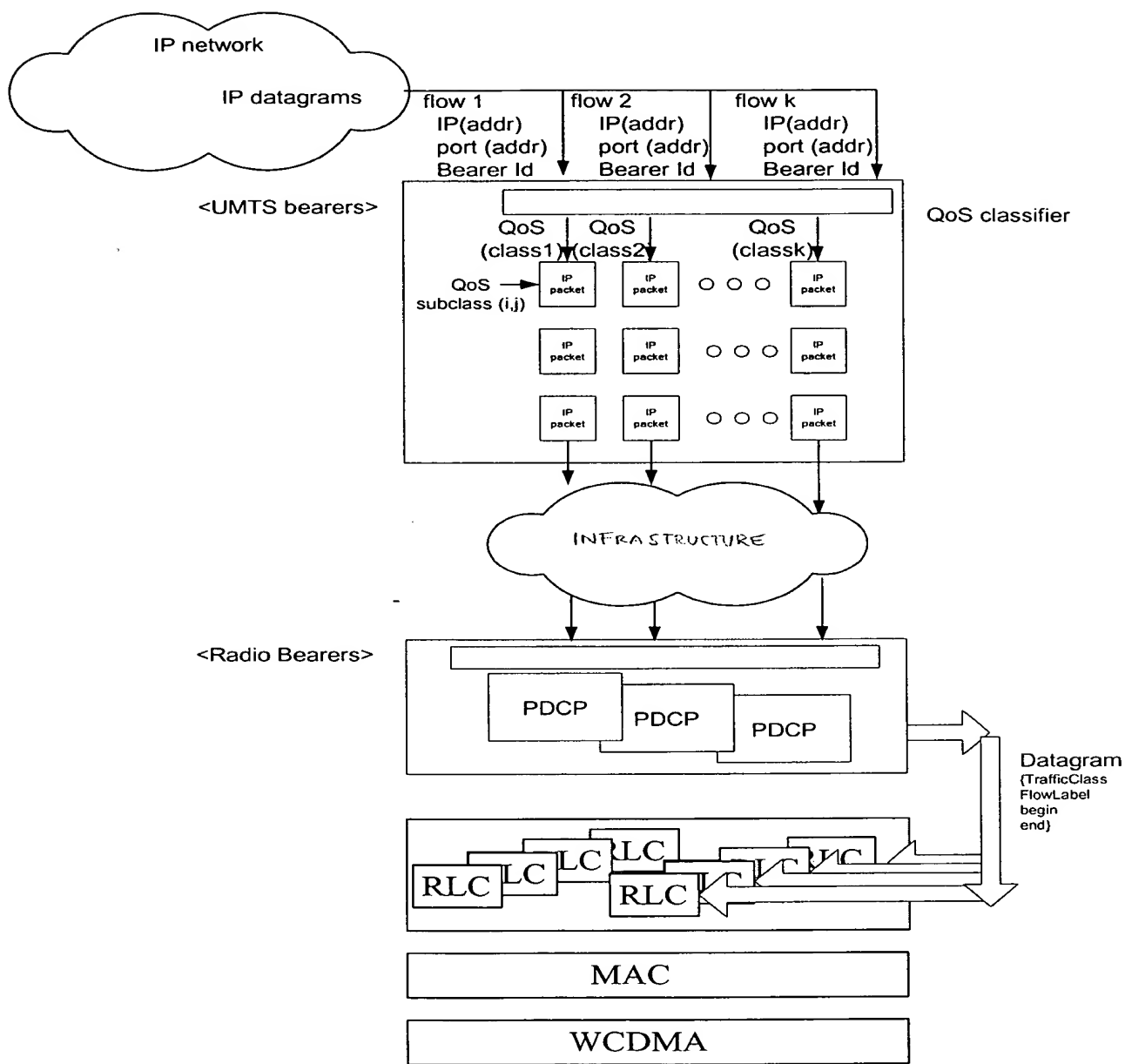
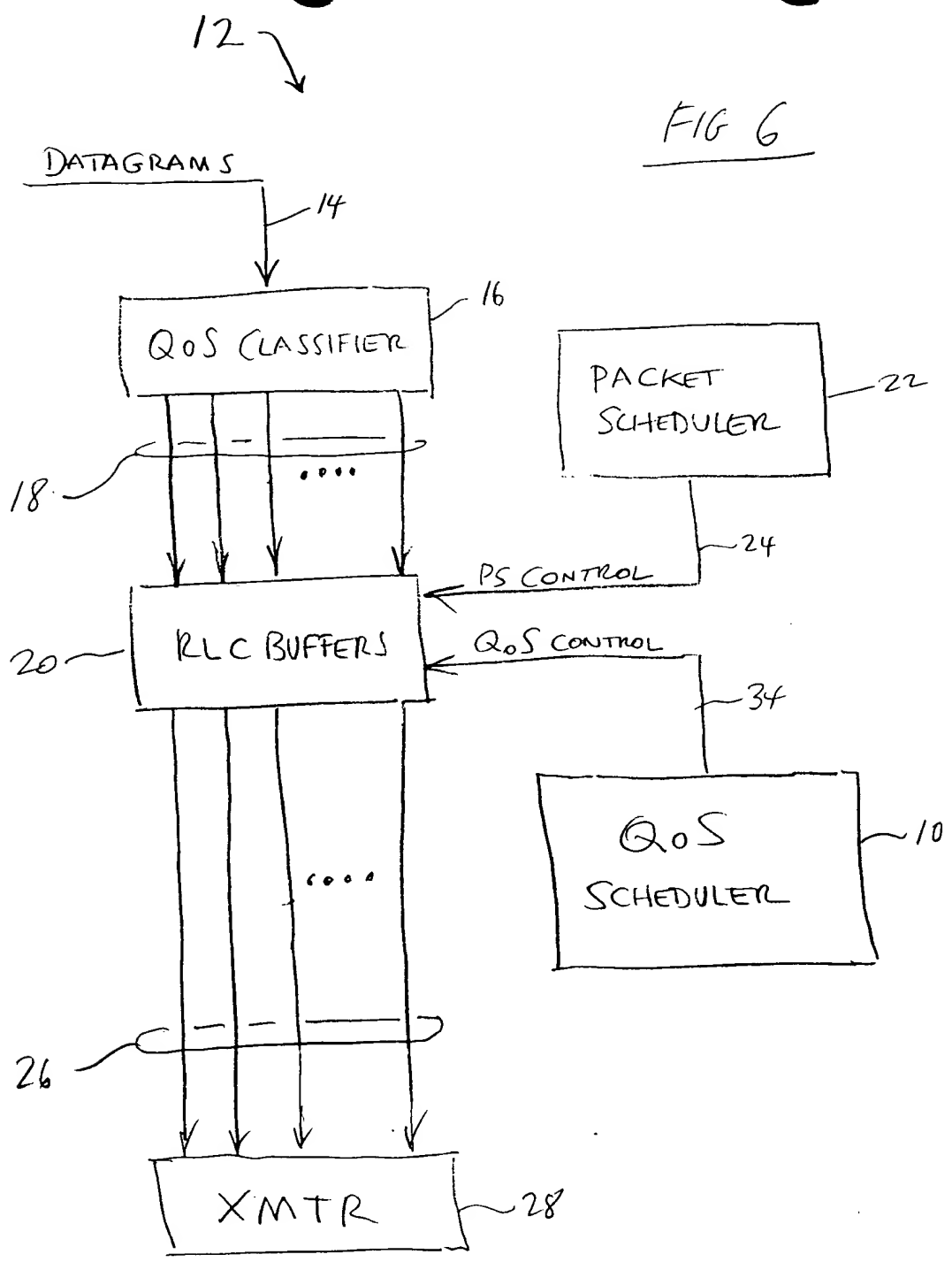
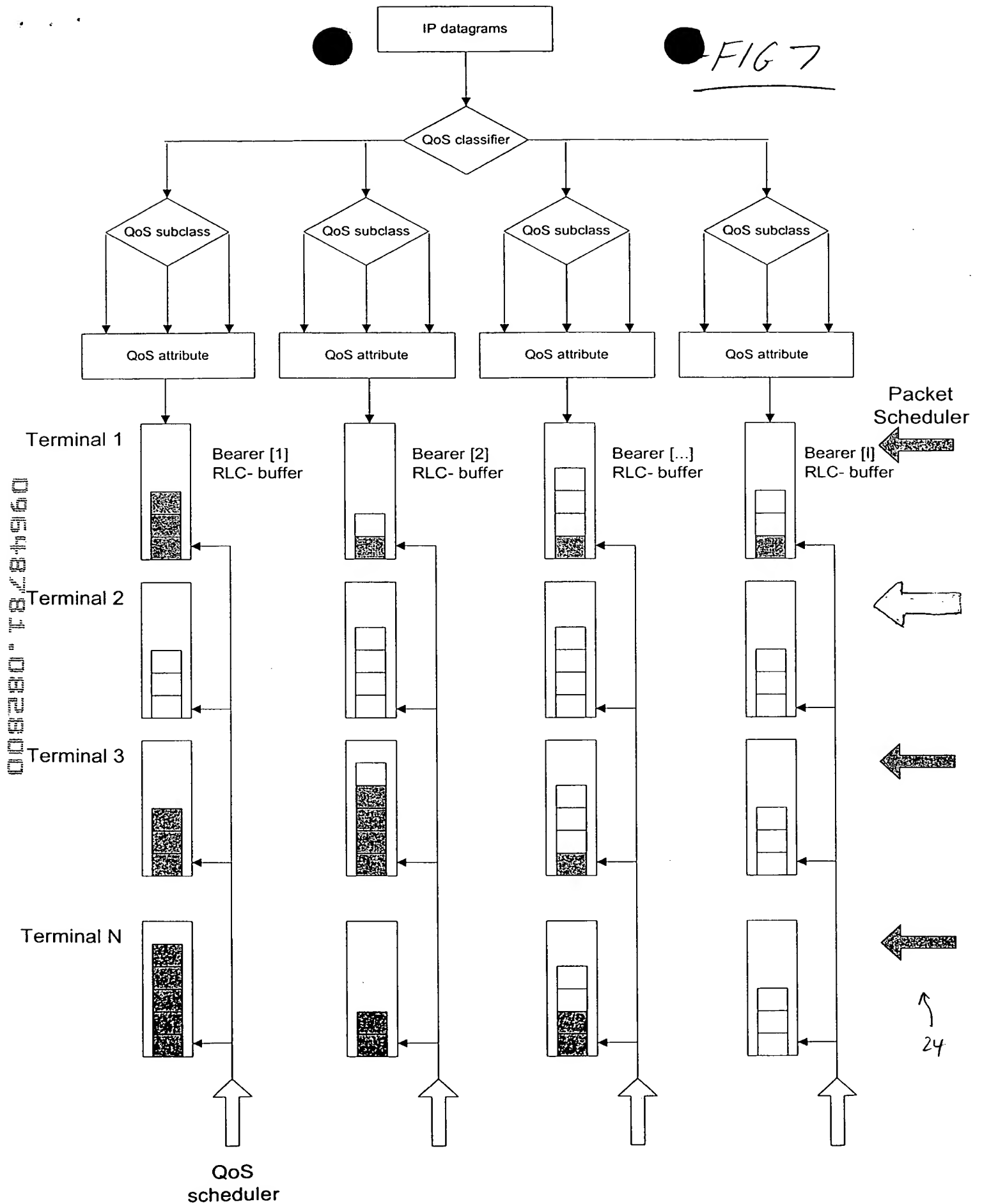


FIG. 5

09648781.082800





The diagram illustrates a packet scheduling method. It starts with a **PacketScheduler** (22) and a **QoS Scheduler** (10). The QoS Scheduler feeds into a **Buffer [1]**. The PacketScheduler feeds into a vertical line (24) which has a **Trigger** at the bottom. The flow proceeds through a series of buffers: **Buffer [1]**, **Buffer [i]**, **Buffer [i+1]**, and **Buffer [I]**. Each buffer is checked for **CapacityLeft**. If the capacity is left (Y), data is sent to a **TransportBlockSet on DCH** (e.g.,  $k_1 * PDU$ ,  $k_i * PDU$ ,  $k_{i+1} * PDU$ ,  $k_I * PDU$ ) and then to **L1 processing**. If the capacity is not left (N), the flow goes to **Next TTI**. The final output is a **Transmission Frame**.

L1  
Trigger

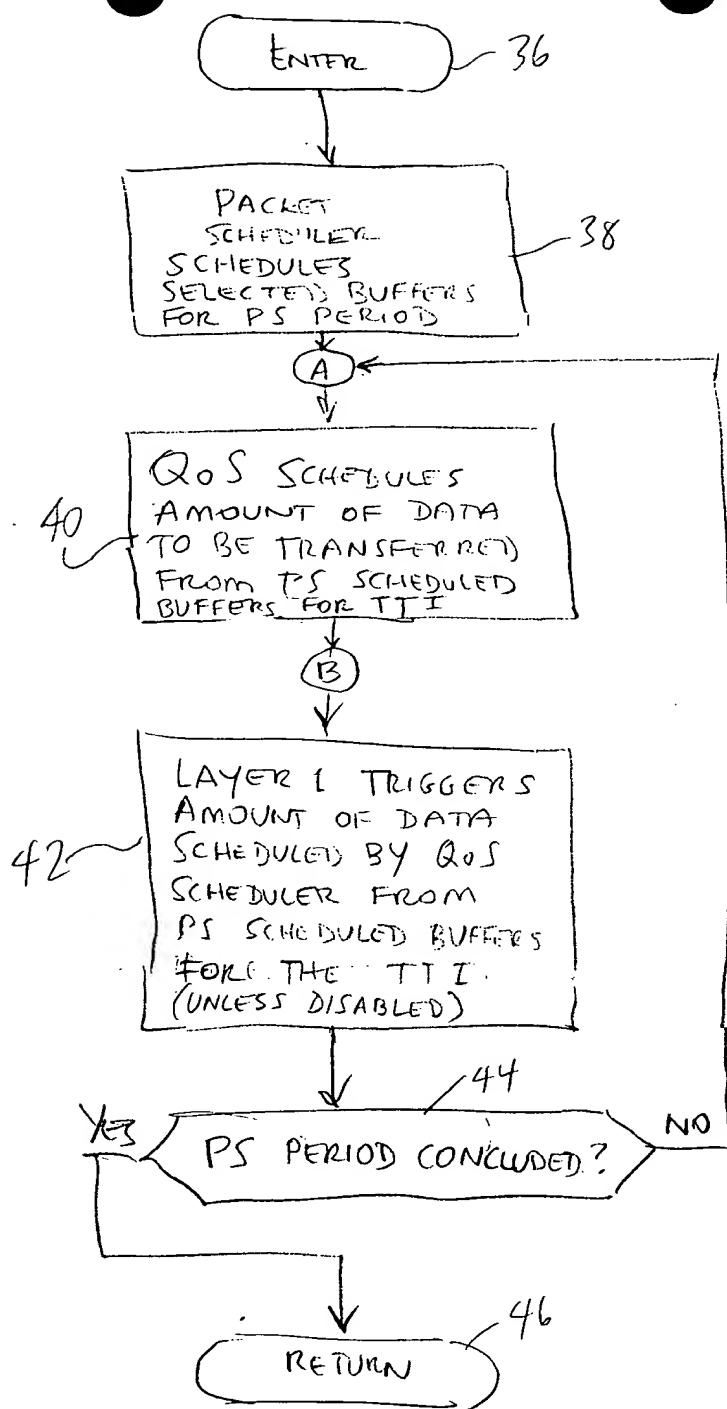
[illegible]

FIG 9



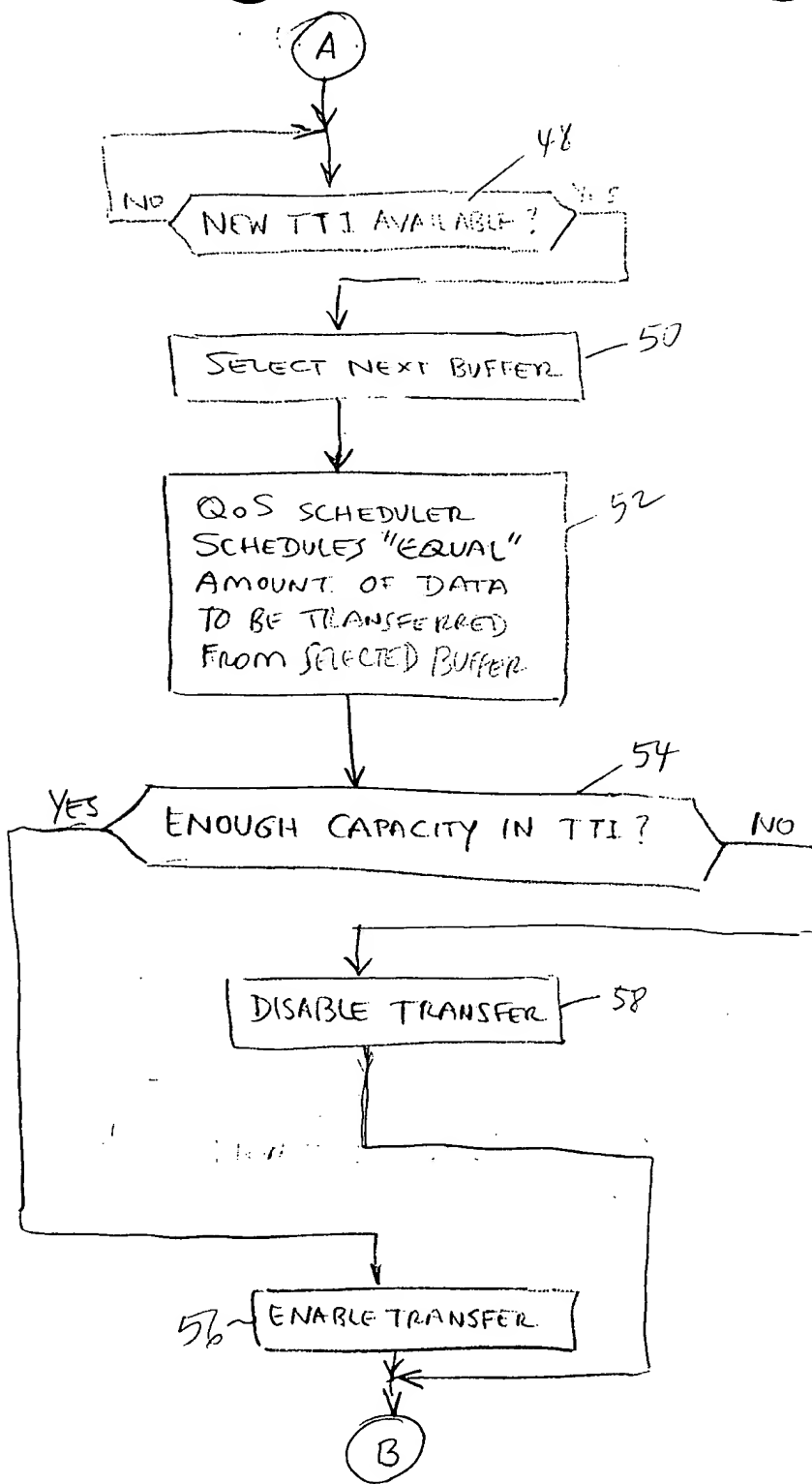


FIG 9A

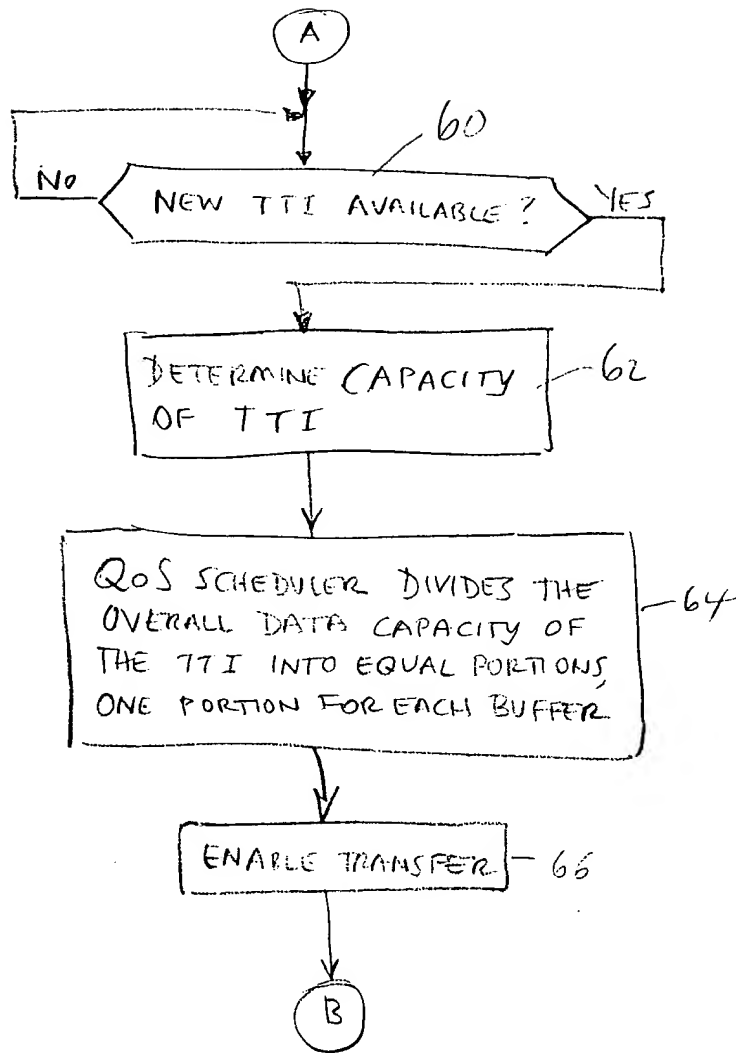


FIG 9B

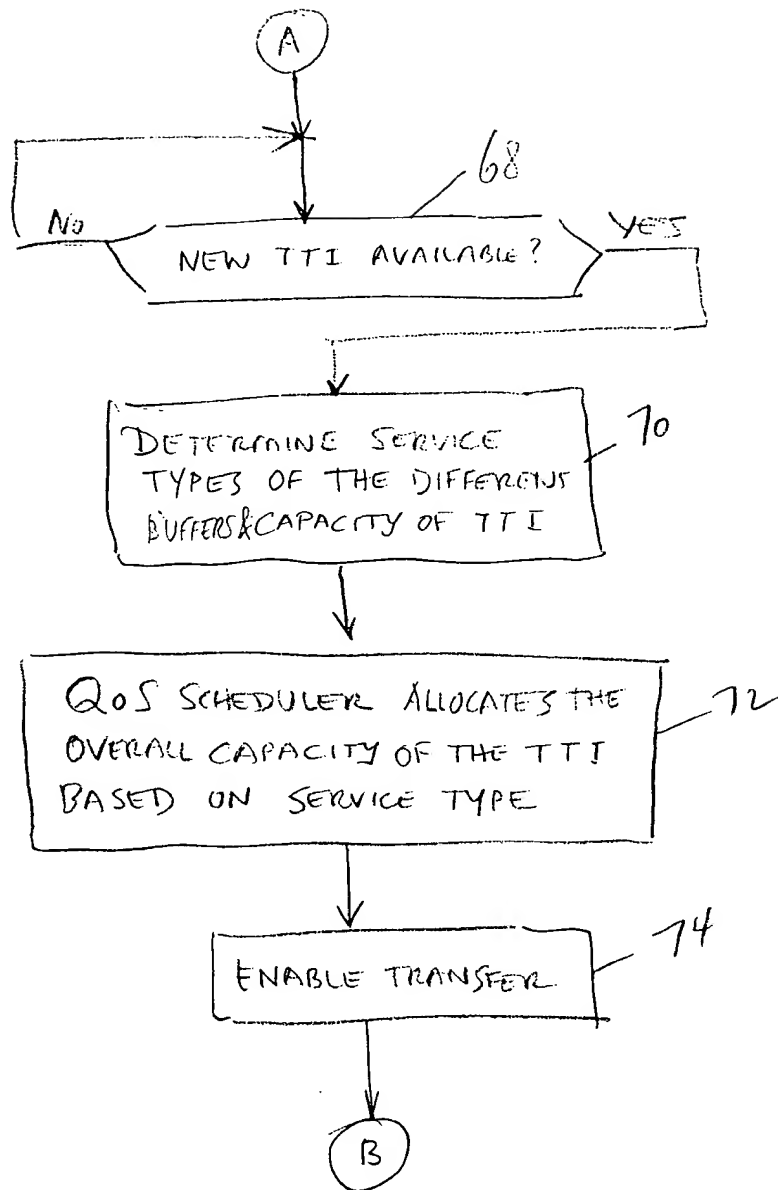


FIG 9C

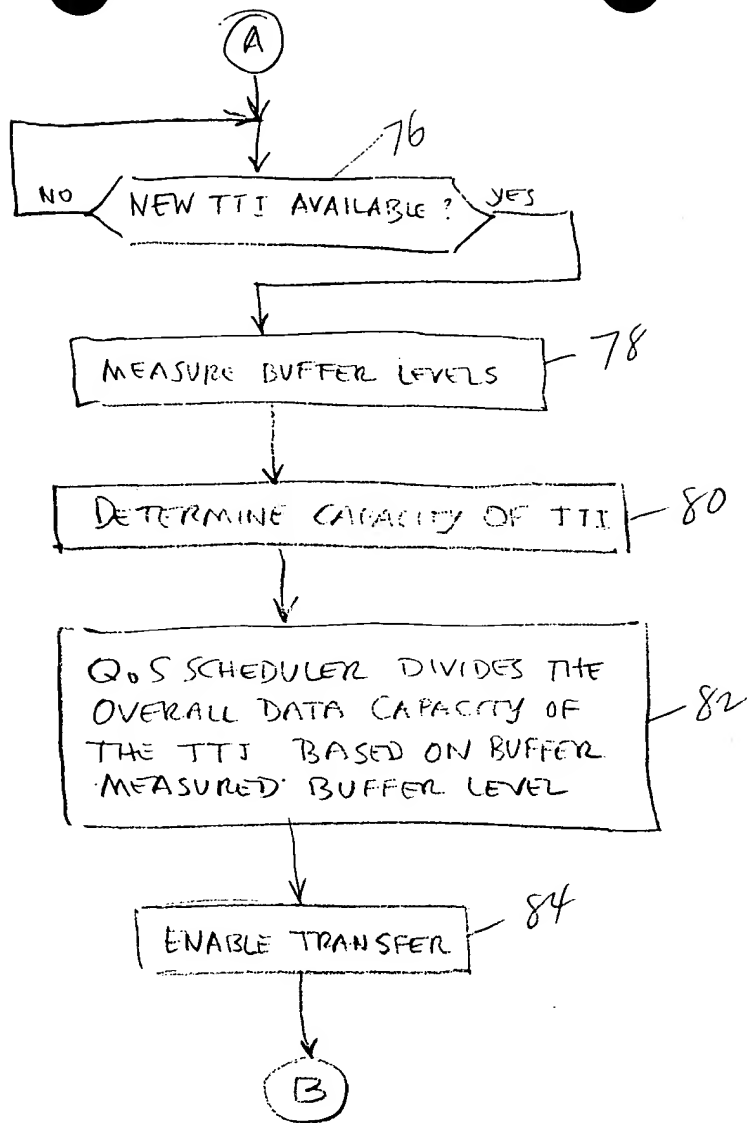


FIG 9D

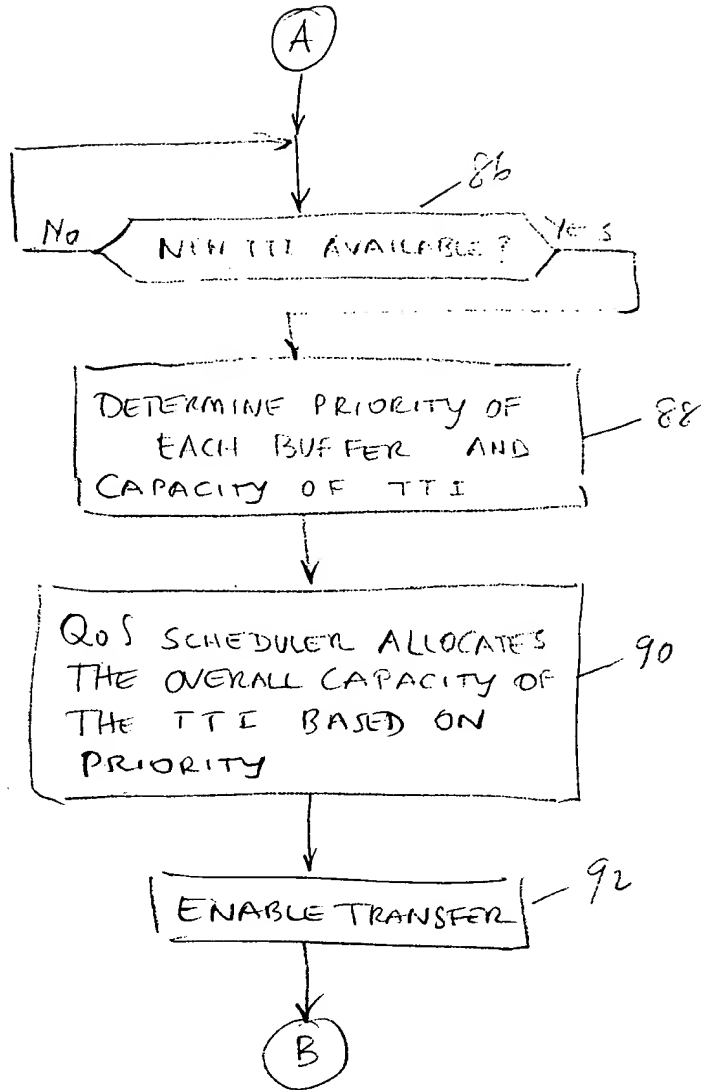


FIG 9E

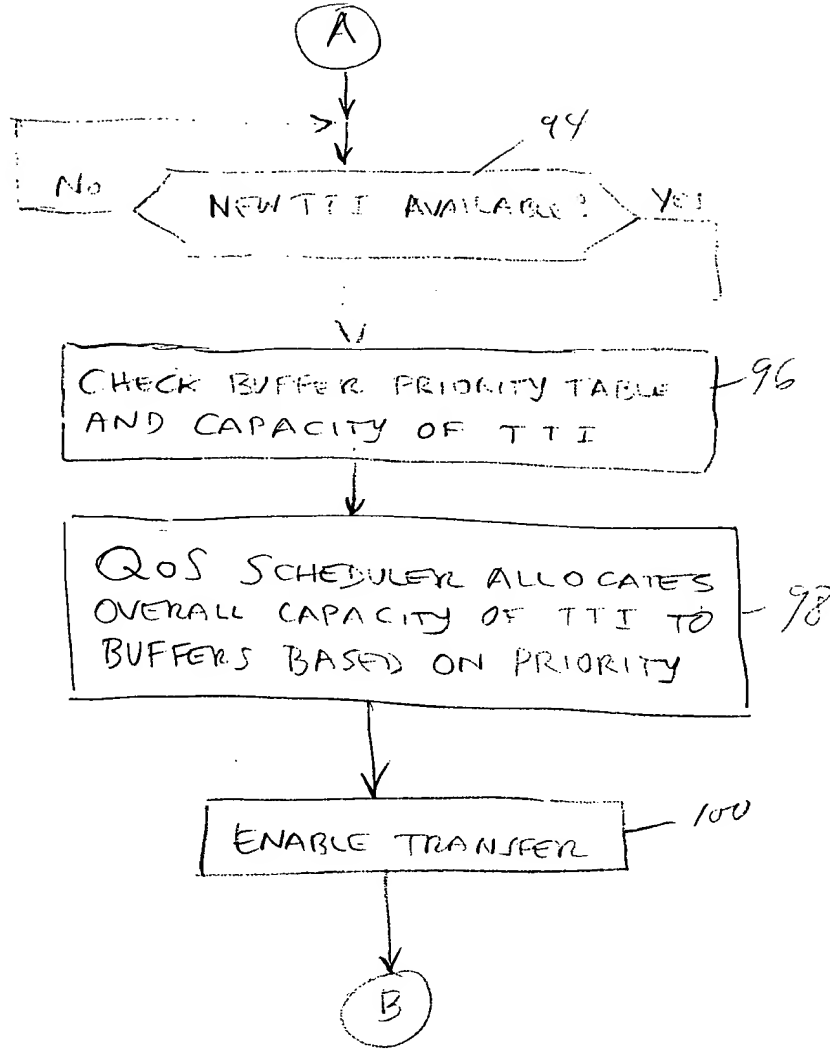


FIG 9F

008280" T8284960

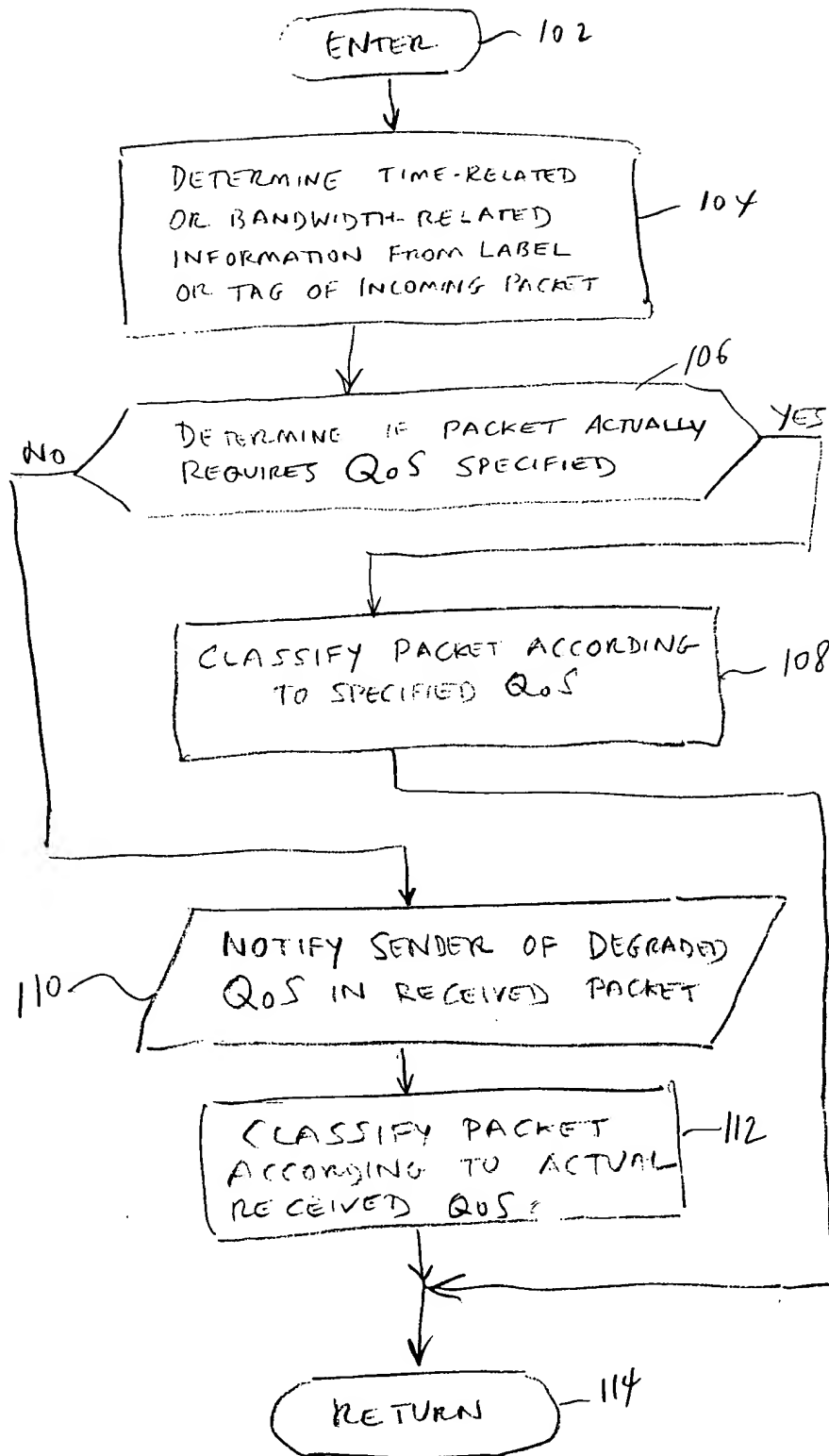
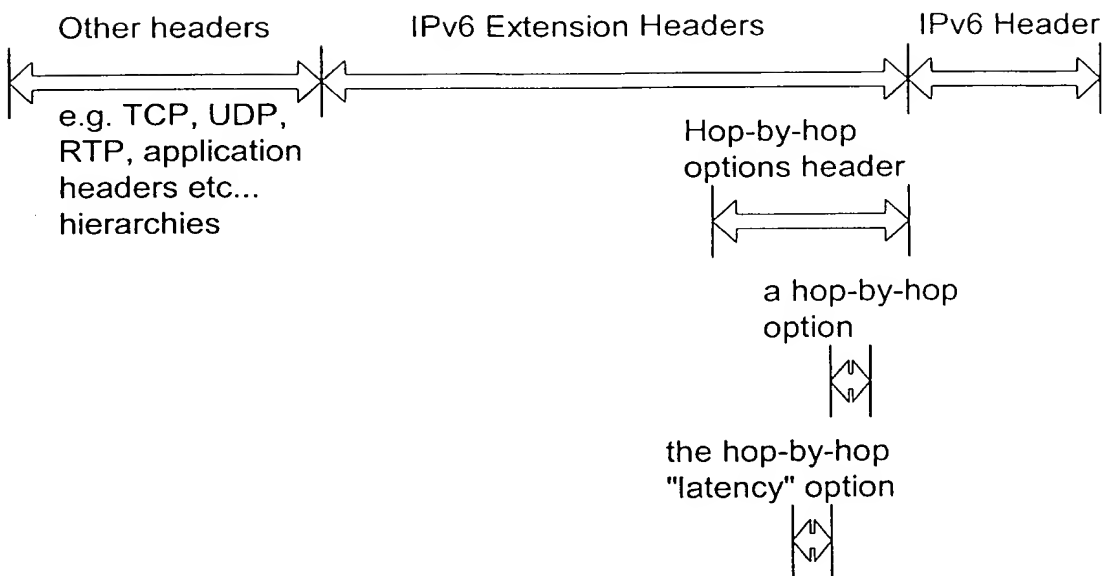


FIG 10

FIG 11



Next Hdr		Hdr ext len	
type	len	value	
type	len	value (latency)	
Next Hdr		Hdr ext len	
Next Hdr		Hdr ext len	

IP datagram

09648781.082800



